



Our design solution incorporates two Atmel products; the AT86RF211 900MHz transceiver chip and the AT90S835 microcontroller. On the transmission side, the microcontroller is used to process user requests, build the message to be sent, and output the final data stream to the transceiver chip, which acts as a pipeline for the data. At the receiver, the microcontroller receives all data from the transceiver, filters out unwanted data (data from seats that do not correspond to that receiver's designated seat), and acts as the control processor for the associated devices.